



PATENTS

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicants: David M. Fried, et al.

Examiner: Unassigned

Serial No: 09/683,328

Art Unit: Unassigned

Filed: December 14, 2001

Docket: BUR920010171US1(15017)

For: IMPLANTED ASYMMETRIC DOPED
POLYSILICON GATE FinFET

Dated: December 20, 2001

Assistant Commissioner for Patents
United States Patent and Trademark Office
Washington, D.C. 20231

INFORMATION DISCLOSURE STATEMENT

Sir:

In accordance with 37 C.F.R. §§ 1.97 and 1.98, it is requested that the following references, which are also listed on the attached Form PTO-1449, be made of record in the above-identified case.

1. U.S. Patent No. 3,872,491, dated March 18, 1975, issued to Hanson, et al.;
2. U.S. Patent No. 5,315,143, dated May 24, 1994, issued to Tsuji;
3. U.S. Patent No. 5,338,959, dated August 16, 1994, issued to Kim, et al.;
4. U.S. Patent No. 5,365,083, dated November 15, 1994, issued to Tada;
5. U.S. Patent No. 5,391,506, dated February 21, 1995, issued to Tada, et al.;
6. U.S. Patent No. 6,031,269, dated February 29, 2000, issued to Liu;
7. U.S. Patent No. 6,069,390, dated May 30, 2000, issued to Hsu, et al.;
8. U.S. Patent No. 6,150,687, dated November 21, 2000, issued to Noble, et al.;

9. U.S. Patent No. 6,207,530 B1, dated March 27, 2001, issued to Hsu, et al.;
10. U.S. Patent No. 6,207,985, dated March 27, 2001, issued to Walker;
11. U.S. Patent No. 6,242,783, dated June 5, 2001, issued to Ohmi, et al.;
12. Leland Chang, et al., "Gate Length Scaling and Threshold Voltage Control of Double-Gate MOSFET's", Department of Electrical Engineering and Computer Sciences, University of California, 2000 IEEE;
13. Keunwoo Kim, et al., "Optimal Double-Gate MOSFET's : Symmetrical or Asymmetrical Gates?", 1999 IEEE International SOI Conference, Oct. 1999, (pp.98-99); and
14. Yan, et al., "Scaling the Si MOSFET : From Bulk to SOI Bulk, IEEE Transactions on Electron Devices, Vol. 39, No. 7, p. 1704, July 1992.

Applicants are submitting copies of the above-cited references.

Inasmuch as this Information Disclosure Statement is being submitted in accordance with the schedule set out in 37 C.F.R. § 1.97(b), no statement or fee is required.

Respectfully submitted,



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